

Form PTO-1449

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Specimen)

TSMC-00-126

Application Number

10/671,259

Applicant

Nai-Yin Sung et al.

Filing Date

09/25/03

Oral Exam Unit

## U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	ALNO DATE IF APPROPRIATE
DS	6026226	2/15/00	Heile et al.	395	500.13	10/27/97
DS	5875111	2/23/99	Patel	364	488	3/25/97
DS	6141631	10/31/00	Blinne et al.	703	14	3/25/98

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

DS	"VHDL Sign-Off Simulation: What Future?", Bakowski, et al., Proceedings of the VHDL Int'l Users Forum, Spring Conf., 1994, IEEE, May 1994, pp. 136-141.
DS	"Standardizing ASIC Libraries in VHDL Using VITAL: a Tutorial", Krotikoski, Proc. of the IEEE 1995 Custom Integrated Circuits Conf., IEEE, May 1995, pp. 603-610.

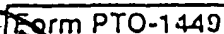
EXAMINER

David S. W.

DATE CONSIDERED

8/26/06

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.



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Doctor Number (Cpsina)

Appleton Museum

TSMIC-00-126

10/671,259

Applicant

Nai-Yin Sung et al.

Filing Date

09/25/03

Group Art Unit

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

11	-	"IEEE Standard for VITAL Application Specific Integrated Circuit (ASIC) Modeling Specification," Design Automation Standards Committee of the IEEE Computer Society, USA, IEEE Std 1076.4-1995, May 1996.

# EXAMEN

David S. W.

DATE CONSIDERED

0/26/06

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Double Number (Circles)

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## INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Portion, Pages, Etc.)

- DRAFT Standard, VITAL ASIC (Application Specific Integrated Circuit) Modeling Specification, Design Automation Standards Committee of the IEEE Computer Society, IEEE P1076.4/D1, April 2000, IEEE Standards Board.

## EXAMINER

David Gilman

DATE CONSIDERED

8/26/06

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